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APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	TOT CLAIMS	IND CLAIMS
10/598,955	09/15/2006	2834	900	SC13308ET	12	2

CONFIRMATION NO. 2522

## FILING RECEIPT

23125  
FREESCALE SEMICONDUCTOR, INC.  
LAW DEPARTMENT  
7700 WEST PARKER LANE MD:TX32/PL02  
AUSTIN, TX78729

Date Mailed: 05/04/2007

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Jerome Enjalbert, Fonsorbes, FRANCE;

## Assignment For Published Patent Application

Freescale Semiconductor, Inc., Austin, TX

Power of Attorney: The patent practitioners associated with Customer Number 23125

## Domestic Priority data as claimed by applicant

This application is a 371 of PCT/EP05/02819 03/15/2005

## Foreign Applications

EUROPEAN PATENT OFFICE (EPO) 04290820.2 03/15/2004

If Required, Foreign Filing License Granted: 05/03/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is  
**US10/598,955**

Projected Publication Date: 08/09/2007

Non-Publication Request: No

Early Publication Request: No

## Title

Low drop-out dc voltage regulator

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